Amendments to the Specification:

Please replace paragraph beginning on page 3, line 26 with the following amended paragraph:

The PWM signals may vary in different embodiments, but in certain embodiments a duty cycle of between approximately 40 and 90% may be used, and in particular embodiments, a duty cycle between approximately 70-85% may be used. In such manner, the LC material may respond with a fast rise time and a fast full fall time.

Please replace paragraph beginning on page 12, line 21 with the following amended paragraph: As shown in FIG. 4, light source 210 is provided to optics 220. Such optics may include, for example, a condensing lens, a shaping lens, and other optical devices. Also, in certain

embodiments, optics 220 may include a color filter or a color switching mechanism, such as a color wheel or color switcher 225, to provide one or more desired colors to LC cell 100. Also in an embodiment in which LC cell 100 is LCOS cell, optics 220 may also include a polarizer to polarize incident light on LC cell 100.

Please replace paragraph beginning on page 13, line 4 with the following amended paragraph:

A driver board 230 may coupled to provide drive signals to LC cell 100 to modulate the incident light into a desired image. In one embodiment, driver board 230 may include a processor 232 and one or more memories 234 and 236. Driver board 230 may be coupled to LC cell 100 via, for example, a flexible cable or the like. In various embodiments, the processor may be a general-purpose microprocessor, or a special-purpose processor such as a microcontroller, application specific integrated circuit (ASIC), a programmable gate array (PGA) and the like. Further, the memory or memories may be static random access memories (SRAMs), in one embodiment. Driver board 230 or another location in display system 200 may include one or more computer programs stored on a storage medium having instructions to operate the system in accordance with an embodiment of the present invention. The storage medium may include, but is not limited to, any type of disk including floppy disks, optical disks, compact disk read-only memories (CD-ROMs), compact disk rewritables (CD-RWs), and magneto-optical disks, semiconductor devices such as read-only memories (ROMs), random access memories (RAMs) such as dynamic and static RAMs, erasable programmable read-only memories (EPROMs),

electrically erasable programmable read-only memories (EEPROMs), flash memories, magnetic or optical cards, or any type of media suitable for storing electronic instructions.

Please replace paragraph beginning on page 14, line 4 with the following amended paragraph:

In operation, the processor <u>232</u> of driver board 230 may provide signals to the one or more memories <u>234 and 236</u> to form a representation of an image. The memories of driver board 230 may act as buffers to store alternating frames of the image. In turn, each memory may be read out to LC cell 100 to enable electrodes controlling the cell to activate the desired pixel elements of the cell. In such manner, frame updates may be provided to LC cell 100, thereby allowing high speed switching of images on LC cell 100.